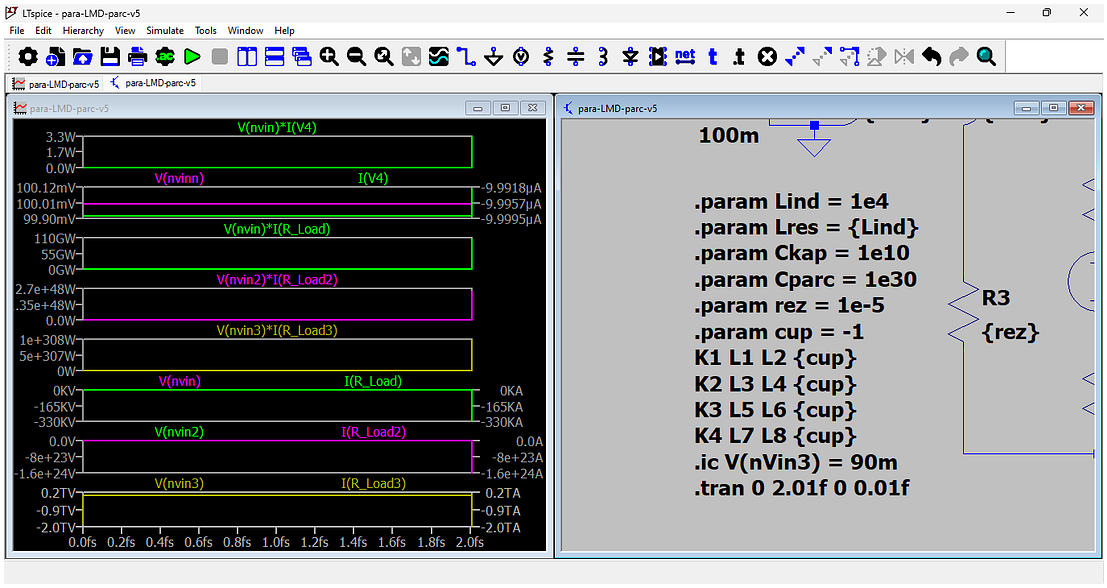
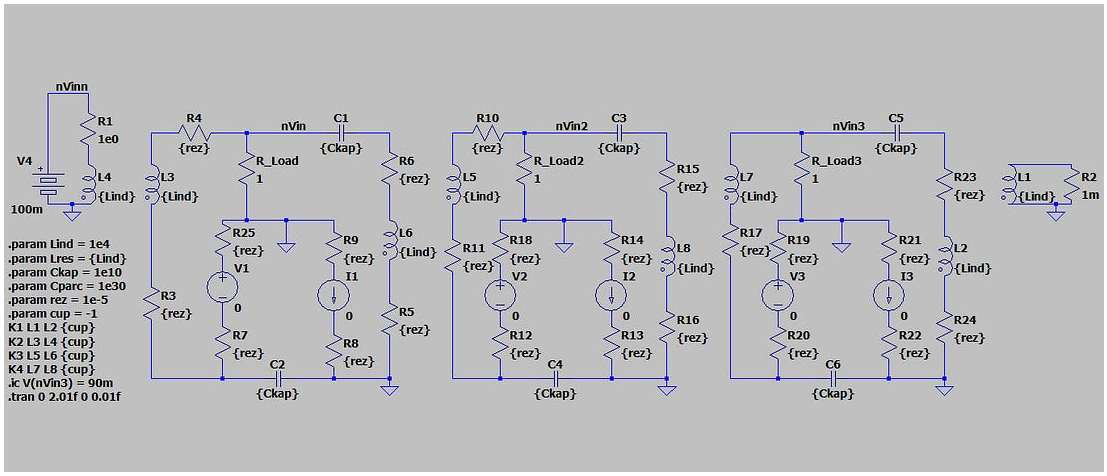
# Parallel Capacitance

VINYASI

APR 29, 2026

Adding parallel capacitance: ‘Cparc’, to the internal parameters of all 6 capacitors:



Netlist > > >

* D:\Documents\Sims\LTSpice\2026\04 - Apr\29\para-LMD-parc-v5.asc

* Generated by LTspice 24.1.9 for Windows.

R_Load nVin 0 1

L1 N007 0 {Lind} Rser={Lres}

L2 N012 N022 {Lind} Rser={Lres}

C1 N002 nVin {Ckap} Cpar={Cparc}

C2 0 N029 {Ckap} Cpar={Cparc}

L3 N001 N019 {Lind} Rser={Lres}

L4 N006 0 {Lind} Rser={Lres}

R2 N007 0 1m

I1 N014 N028 0

V1 N013 N023 0

R_Load2 nVin2 0 1

L5 N003 N010 {Lind} Rser={Lres}

C3 N004 nVin2 {Ckap} Cpar={Cparc}

C4 0 N030 {Ckap} Cpar={Cparc}

L6 N008 N020 {Lind} Rser={Lres}

I2 N016 N025 0

V2 N015 N024 0

R_Load3 nVin3 0 1

C5 N005 nVin3 {Ckap} Cpar={Cparc}

C6 0 N031 {Ckap} Cpar={Cparc}

L7 nVin3 N009 {Lind} Rser={Lres}

I3 N018 N027 0

V3 N017 N026 0

L8 N011 N021 {Lind} Rser={Lres}

V4 nVinn 0 100m

R1 nVinn N006 1e0

R3 N019 N029 {rez}

R4 nVin N001 {rez}

R5 N020 0 {rez}

R6 N002 N008 {rez}

R7 N023 N029 {rez}

R8 N028 0 {rez}

R9 0 N014 {rez}

R10 nVin2 N003 {rez}

R11 N010 N030 {rez}

R12 N024 N030 {rez}

R13 N025 0 {rez}

R14 0 N016 {rez}

R15 N004 N011 {rez}

R16 N021 0 {rez}

R17 N009 N031 {rez}

R18 0 N015 {rez}

R19 0 N017 {rez}

R20 N026 N031 {rez}

R21 0 N018 {rez}

R22 N027 0 {rez}

R23 N005 N012 {rez}

R24 N022 0 {rez}

R25 0 N013 {rez}

.param Lind = 1e4

.param Lres = {Lind}

.param Ckap = 1e10

.param Cparc = 1e30

.param rez = 1e-5

.param cup = -1

K1 L1 L2 {cup}

K2 L3 L4 {cup}

K3 L5 L6 {cup}

K4 L7 L8 {cup}

.ic V(nVin3) = 90m

.tran 0 2.01f 0 0.01f

.backanno

.end

Log file > > >

LTspice 24.1.9 for Windows

Circuit: D:\Documents\Sims\LTSpice\2026\04 - Apr\29\para-LMD-parc-v5.net

Start Time: Wed Apr 29 11:39:43 2026

solver = Normal

Maximum thread count: 4

tnom = 27

temp = 27

method = trap

Direct Newton iteration for .op point succeeded.

Total elapsed time: 0.851 seconds.

Files loaded:

D:\Documents\Sims\LTSpice\2026\04 - Apr\29\para-LMD-parc-v5.net

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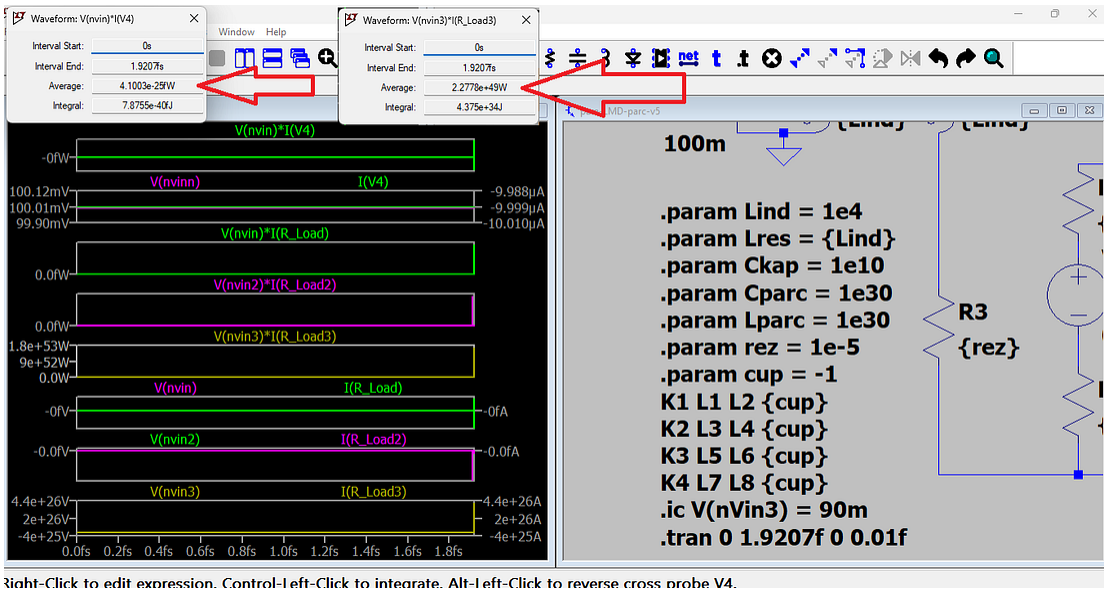
Here, parallel capacitance has been added to both coils as well as caps > > >




... resulting in the input wattage (coming from the battery) and the output wattage at R_Load3 are both positive indicating that the battery is receiving a little energy from the circuit while the resistive load, R_Load3, is receiving lots of energy.

But, in fiddling with this circuit, it is obvious that it is merely a transient condition of overunity quickly dropping to near zero making it a non-sustained surge.

Netlist > > >

```
* D:\Documents\Sims\LTSpice\2026\04 - Apr\29\para-LMD-parc-v5.asc

* Generated by LTspice 24.1.9 for Windows.

R_Load nVin 0 1

L1 N006 0 {Lind} Rser={Lres} Cpar={Lparc}

L2 N011 N021 {Lind} Rser={Lres} Cpar={Lparc}

C1 N002 nVin {Ckap} Cpar={Cparc}

C2 0 N028 {Ckap} Cpar={Cparc}

L3 N001 N018 {Lind} Rser={Lres} Cpar={Lparc}

L4 N005 0 {Lind} Rser={Lres} Cpar={Lparc}

R2 N006 0 1m

I1 N013 N027 0

V1 N012 N022 0

R_Load2 nVin2 0 1

L5 0 N009 {Lind} Rser={Lres} Cpar={Lparc}

C3 N003 nVin2 {Ckap} Cpar={Cparc}

C4 N030 N029 {Ckap} Cpar={Cparc}

L6 N007 N019 {Lind} Rser={Lres} Cpar={Lparc}

I2 N015 N024 0

V2 N014 N023 0
```

R_Load3 nVin3 0 1

C5 N004 nVin3 {Ckap} Cpar={Cparc}

C6 0 N031 {Ckap} Cpar={Cparc}

L7 nVin3 N008 {Lind} Rser={Lres} Cpar={Lparc}

I3 N017 N026 0

V3 N016 N025 0

L8 N010 N020 {Lind} Rser={Lres} Cpar={Lparc}

V4 nVinn 0 100m

R1 nVinn N005 1e0

R3 N018 N028 {rez}

R4 nVin N001 {rez}

R5 N019 0 {rez}

R6 N002 N007 {rez}

R7 N022 N028 {rez}

R8 N027 0 {rez}

R9 0 N013 {rez}

R10 nVin2 0 {rez}

R11 N009 N029 {rez}

R12 N023 N029 {rez}

R13 N024 N030 {rez}

R14 0 N015 {rez}

R15 N003 N010 {rez}

R16 N020 N030 {rez}

R17 N008 N031 {rez}

R18 0 N014 {rez}

R19 0 N016 {rez}

R20 N025 N031 {rez}

R21 0 N017 {rez}

R22 N026 0 {rez}

R23 N004 N011 {rez}

R24 N021 0 {rez}

R25 0 N012 {rez}

.param Lind = 1e4

.param Lres = {Lind}

.param Ckap = 1e10

.param Cparc = 1e30

.param Lparc = 1e30

.param rez = 1e-5

.param cup = -1

K1 L1 L2 {cup}

K2 L3 L4 {cup}

K3 L5 L6 {cup}

K4 L7 L8 {cup}

.ic V(nVin3) = 90m

.tran 0 1.9207f 0 0.01f

.backanno

.end

Output log > > >

LTspice 24.1.9 for Windows

Circuit: D:\Documents\Sims\LTSpice\2026\04 - Apr\29\para-LMD-parc-v5.net

Start Time: Wed Apr 29 12:01:47 2026

solver = Normal

Maximum thread count: 4

tnom = 27

temp = 27

method = trap

WARNING: Node n020 is floating.

Direct Newton iteration for .op point succeeded.

Total elapsed time: 0.302 seconds.

Files loaded:

D:\Documents\Sims\LTSpice\2026\04 - Apr\29\para-LMD-parc-v5.net

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Download this last simulation here.

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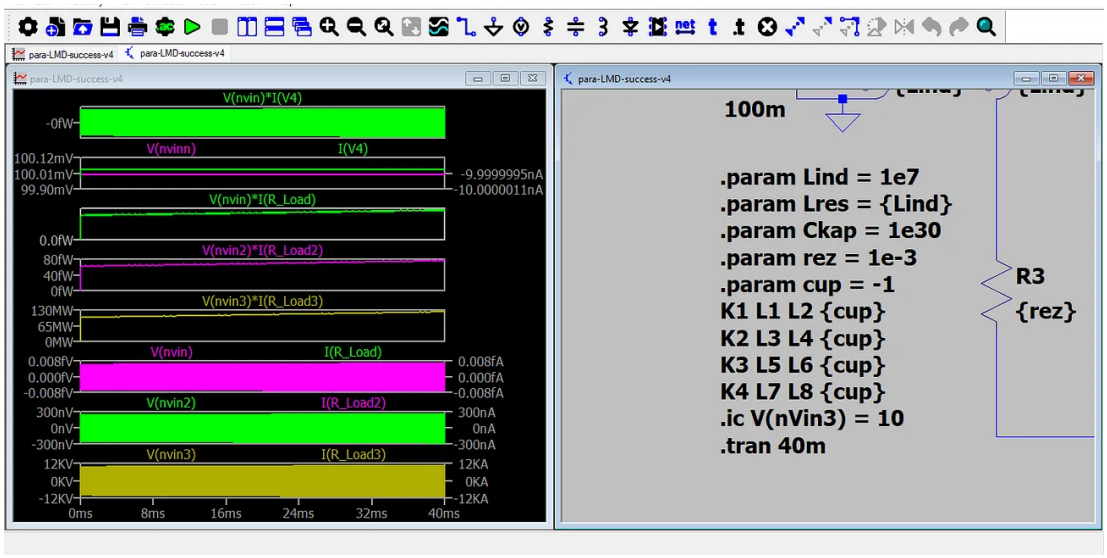
The previous post to this idea was here > > >

# Parametric LMD, success, v4!

VINYASI · APR 29




You'll notice, in the screenshot above, that the input wattage in the topmost graph is so low that LTSpice fails to measure it in any units that the simulator is aware of. Meanwhile, the bottom-most …

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