

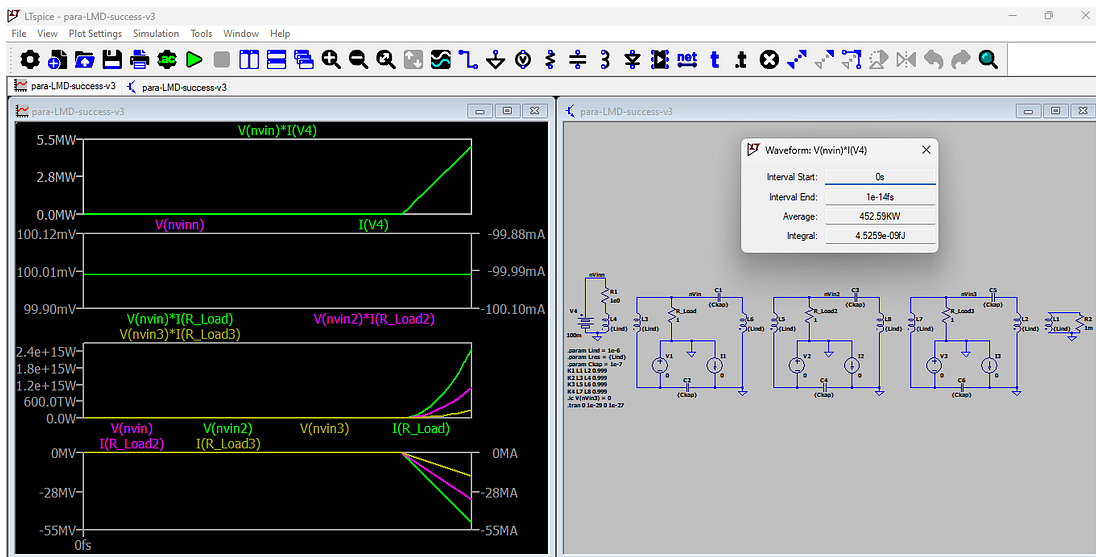
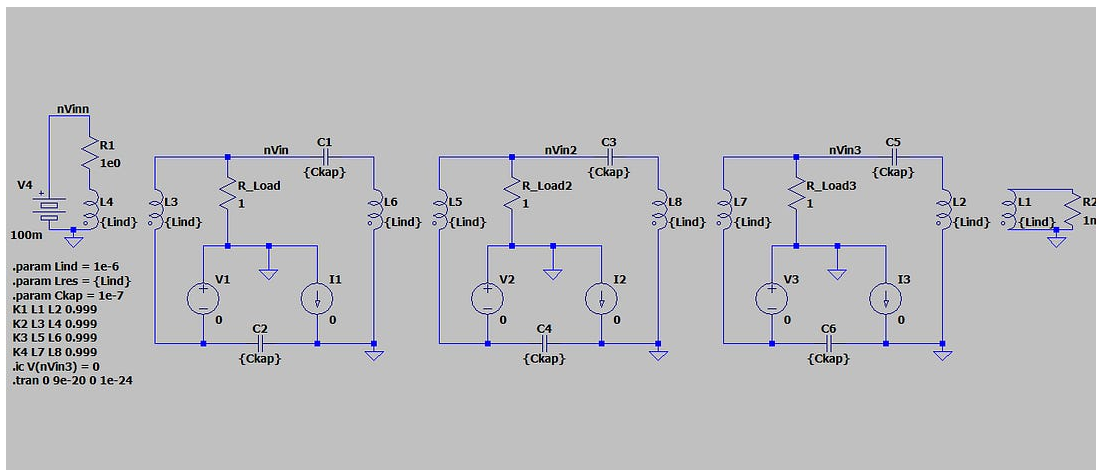
# Parametric LMD, success, v3?



VINYASI

APR 28, 2026

Schematic:



Right-Click to edit expression. Control-Left-Click to integrate. Alt-Left-Click to reverse cross probe V4.

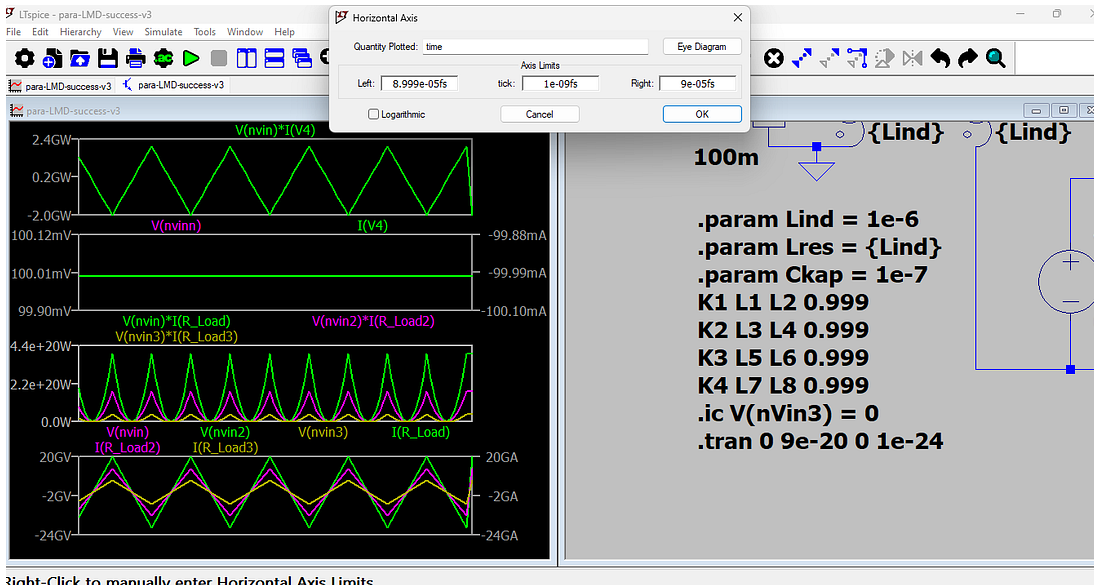
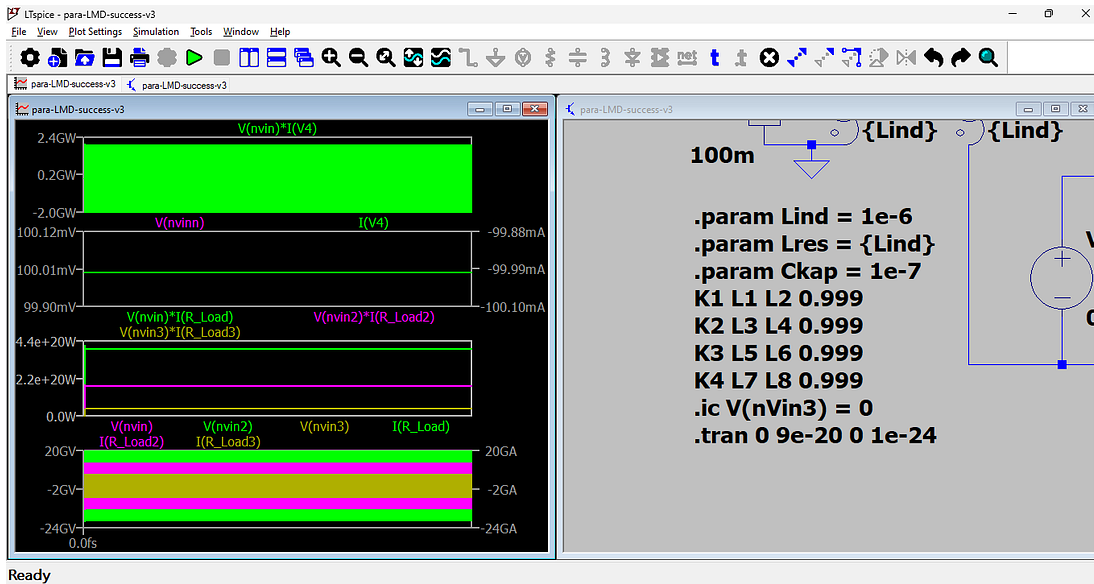
If we examine this screenshot, above, we may notice that the three resistive loads in the last two graphs demonstrates that, while they are rising, they are showing a discrepancy among each other of a time lag in which R\_Load is leading the other two, and R\_Load2 is leading R\_Load3. Since the energy is coming from the far-left side of the schematic, above, from a 100mV battery, I'm left to conclude that Eric Dollard's analog computer in LMD mode does not prohibit TEM (transverse magneto-dielectric) waves from propagating across this circuit from left to right.

So, there ends my hypothetical fantasy!

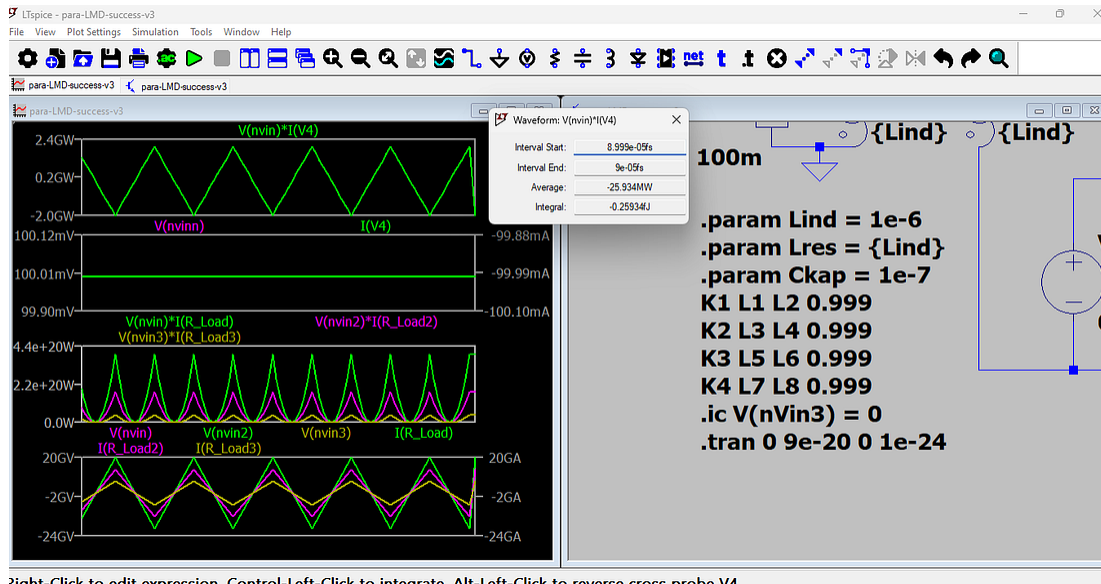
Now, I may be wrong in that this assessment may be failing to take into consideration the TEM influence which the pair of voltage and current sources are adding to the behavior of this circuit. They are located in the center of each of these three modules and are shorting out each of them.

So, like my prior post, this may be inconclusive for proving whether or not this type of circuit can test for the existence and usefulness of LMD waves.

9e-20 second duration of runtime in all of these images  
(0.00000000000000000009 seconds):



1e-23 second window, beginning at 8.999e-05fs and terminating at 9e-20s, above and below:



Right-Click to edit expression. Control-Left-Click to integrate. Alt-Left-Click to reverse cross probe V4.

Input from the battery (V4), above. Output from three resistors, below, added together:

start: 8.999e-05fs

end: 9e-20s

battery: V4

-25.934MW

-0.25934fJ

R\_Load:

1.2899e+20W

1.2899mJ

R\_Load2:

5.7138e+19W

571.38 $\mu$ J

R\_Load3:

1.4256e+19W

142.56 $\mu$ J

2e+28W benefit

2.6e+7W cost

## **7.69e+20 to 1 COP (gain)**

But is this gain the byproduct of a momentary surge that will slowly, but surely, dissipate this initial rise of power? I don't know since I can't run this simulation long enough to see what happens next. Does it begin to diminish and maintain its diminishment over a long stretch of time? That would be indicative of a transient which are quite commonplace.

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Netlist > > >

\* C:\Users\vinya\Documents\Sims\LTSpice\2026\04 - Apr\28\para-LMD-success-v3.asc

\* Generated by LTspice 24.1.9 for Windows.

R\_Load nVin 0 1

L1 N005 0 {Lind} Rser={Lres}

L2 N003 0 {Lind} Rser={Lres}

C1 N001 nVin {Ckap}

C2 0 N006 {Ckap}

L3 nVin N006 {Lind} Rser={Lres}

L4 N004 0 {Lind} Rser={Lres}

R2 N005 0 1m

I1 0 0 0

V1 0 N006 0

R\_Load2 nVin2 0 1

L5 nVin2 N007 {Lind} Rser={Lres}

C3 N002 nVin2 {Ckap}

C4 0 N007 {Ckap}

L6 N001 0 {Lind} Rser={Lres}

I2 0 0 0

V2 0 N007 0

R\_Load3 nVin3 0 1

C5 N003 nVin3 {Ckap}

C6 0 N008 {Ckap}

L7 nVin3 N008 {Lind} Rser={Lres}

I3 0 0 0

V3 0 N008 0

L8 N002 0 {Lind} Rser={Lres}

V4 nVinn 0 100m

R1 nVinn N004 1e0

.param Lind = 1e-6

.param Lres = {Lind}

.param Ckap = 1e-7

K1 L1 L2 0.999

K2 L3 L4 0.999

K3 L5 L6 0.999

K4 L7 L8 0.999

.ic V(nVin3) = 0

.tran 0 9e-20 0 1e-24

.backanno

.end

Output log > > >

LTspice 24.1.9 for Windows

Circuit: C:\Users\vinya\Documents\Sims\LTSpice\2026\04 - Apr\28\para-LMD-success-v3.net

Start Time: Tue Apr 28 12:02:02 2026

solver = Normal

Maximum thread count: 4

tnom = 27

temp = 27

method = trap

Direct Newton iteration for .op point succeeded.

Total elapsed time: 2.478 seconds.

Files loaded:

C:\Users\vinya\Documents\Sims\LTSpice\2026\04 - Apr\28\para-LMD-success-v3.net

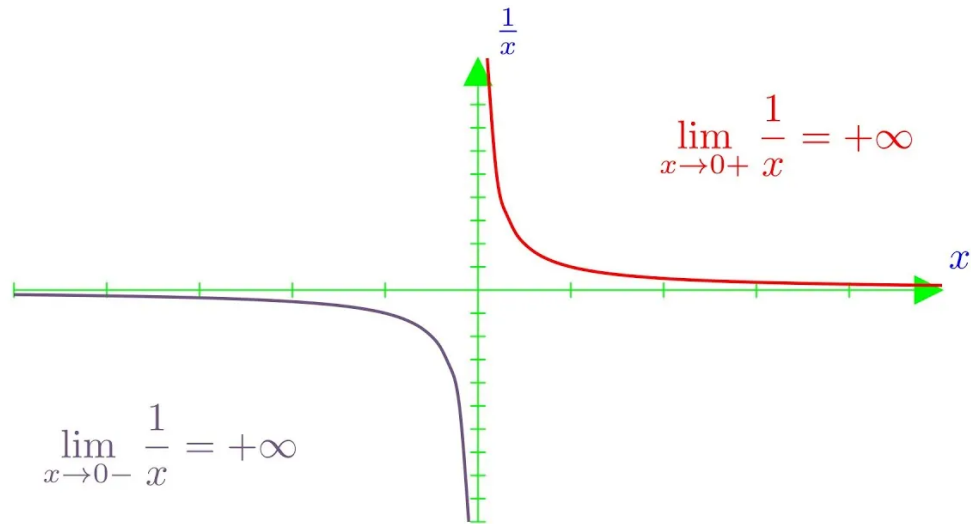
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[Download this circuit.](#)

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# **The Limitations of my Laptop Fail to Definitively Confirm the Existence of Longitudinal (Magneto-Dielectric) Waves.**

VINYASI · APR 28



I mention “hyperbolic” in the audio, above, when I should have mentioned the mathematical limit of  $1/x$ , or  $x/y$  as examples of: inductance divided by capacitance.

[Read full story](#)